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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/005,936	12/03/2001	Jinsaku Masuyama	016295.0733 (DC-03225)	7808

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Adam L. Stroud
Baker Botts L.L.P.
One Shell Plaza
910 Louisiana
Houston, TX 77002-4995

EXAMINER

CHEN, TSE W

ART UNIT	PAPER NUMBER
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2116

DATE MAILED: 07/14/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary

Application No.

10/005,936

Applicant(s)

MASUYAMA ET AL.

Examiner

Tse Chen

Art Unit

2116

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 13 April 2005.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-25 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) _____ is/are allowed.
- 6) ☒ Claim(s) 1-25 is/are rejected.
- 7) ☐ Claim(s) _____ is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on _____ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some * c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
 2. ☐ Certified copies of the priority documents have been received in Application No. _____.
 3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- | | |
|--|---|
| 1) <input type="checkbox"/> Notice of References Cited (PTO-892) | 4) <input type="checkbox"/> Interview Summary (PTO-413)
Paper No(s)/Mail Date. _____ |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | 5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152) |
| 3) <input type="checkbox"/> Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)
Paper No(s)/Mail Date _____ | 6) <input type="checkbox"/> Other: _____ |

DETAILED ACTION

Continued Examination Under 37 CFR 1.114

1. A request for continued examination under 37 CFR 1.114, including the fee set forth in 37 CFR 1.17(e), was filed in this application after final rejection. Since this application is eligible for continued examination under 37 CFR 1.114, and the fee set forth in 37 CFR 1.17(e) has been timely paid, the finality of the previous Office action has been withdrawn pursuant to 37 CFR 1.114. Applicant's submission filed on April 13, 2005 has been entered.

2. Claims 1-25 are presented for examination.

Claim Rejections - 35 USC § 103

3. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

4. Claims 1-9, 11, 14-16, 18-23, and 25 are rejected under 35 U.S.C. 103(a) as being unpatentable over Smith, U.S. Publication 2002/0198608, in view of Tsurumi, U.S. Patent 5915122, and Bottom, U.S. Publication 2002/0194412.

5. In re claim 1, Smith discloses a computer system [fig.2; multiple processor combination 100] comprising:

- At least two server modules [server blades 102-1 and 102-n; fig.2].
- A midplane [pci bus 110] associated with the at least two server modules, the midplane operable to receive the at least two server modules and to provide a unique address for

Art Unit: 2116

each server module based on the location of each server module on the midplane [0025-0026].

- At least one address module [system server blade 102-0] associated with the at least two server modules, the address module operable to obtain the unique address from the midplane for each server module [0029; determines unique address of each server module based on GA pins].

6. Smith did not disclose explicitly a power supply associated with the midplane and did not discuss the sequential supply of power to the server modules.

7. Bottom discloses a computer system [modular server system 100; fig.1] comprising:

- At least one power supply [130] associated with the midplane, the power supply operable to supply power to start up the server modules [server blades 110 require power in order to start up] [0021].

8. Bottom did not discuss the sequential supply of power to the server modules.

9. Tsurumi discloses a computer system [col.1, ll.10-30] comprising:

- At least one module [cross control circuit X] operable to calculate a start-up time [timer value] for each module [battery, power controller] based on the unique address for each module [col.38, l.56 – col.39, l.6].
- At least one module [cross control circuit X] operable to sequence power to the server modules based on the start-up times for each of the modules [col.38, l.56 – col.39, l.17; battery test expends power].

10. It would have been obvious to one of ordinary skill in the art, having the teachings of Smith, Bottom and Tsurumi before him at the time the invention was made, to modify the system

Art Unit: 2116

taught by Smith to include the power supply taught by Bottom, in order to provide a more efficient use of the resources and minimize space [Bottom: 0021]. Furthermore, one of ordinary skill in the art would have modify the system taught by Smith and Bottom to include the sequential power supply teachings of Tsurumi, in order to provide a system comprising at least one address module associated with the at least two server modules, the address module operable to obtain the unique address from the midplane for each server module and to calculate a start-up time for each server module based on the unique address for each server module and at least one power supply associated with the midplane, the power supply operable to sequence power to the server modules based on the start-up times for each of the server modules. One of ordinary skill in the art would have been motivated to make such a combination as it provides a more efficient use of the resources and minimize space [Bottom: 0021] [Tsurumi: col.38, ll.4-15; col.39, ll.7-17].

11. As to claim 2, Smith discloses the system wherein the server modules comprise blade servers [incorporate multiple 102 blade servers as one server module as it is well known in the art to incorporate a plurality of servers into one module].

12. As to claim 3, Examiner takes Official Notice that brick and blade servers are known equivalents. One of ordinary skill in the art would have chosen either brick or blade server based on the configuration requirements of the system. Accordingly, it would have been obvious to replace blade servers with brick servers to be sequentially powered up in an autonomous fashion.

13. As to claim 4, Smith discloses the system wherein the midplane comprises a circuit board including two or more connectors [multiple compact pci connectors for multiple modules] coupled to the midplane and two or more resistors [some kind of resistors for the multiple GA

Art Unit: 2116

pin is needed or a short may occur] coupled to the midplane [0025]. Regarding the resistors, the Examiner has taken Official Notice that it is well known in the art to use pull-up resistors to signify a certain bit of information. One of ordinary skill in the art would have been motivated to make such a combination in order to provide status information in an electrical interconnection.

14. As to claim 5, Bottom discloses the system wherein the connectors [compact pci form factor interface 660] are operable to provide an interface between the server modules [110] and the midplane [170] [0033].

15. As to claim 6, Smith discloses the system wherein each connector [compact pci connector 112] is operable to interface with one server module [fig. 1; 0025].

16. As to claim 7, Smith discloses the system wherein the midplane provides a unique address to each server module through resistor strapping the one or more resistors [0025].

17. As to claim 8, Bottom discloses the system wherein the midplane is further operable to provide an interface [some kind of interface in broadest interpretation is needed to provide power and switching between redundant power supplies] between the server modules and the power supply [0021].

18. As to claim 9, Tsurumi discloses the system wherein the power supply is operable to provide power to each server module upon expiration of the start-up time for each server module [col.38, l.56 – col.39, l.17; sequencing power is supplying power].

19. As to claim 11, Tsurumi discloses the system wherein each address module [cross control circuit X] includes a timer, the address module further operable to set the timer with the start-up time and the timer operable to count to [down from] the start-up time [col.38, l.56 – col.39, l.6].

Regarding the timer that is operable to count down from the start-up time, the Examiner has

Art Unit: 2116

taken Official Notice that it is well known in the art to configure a timer to either count down or count to a known time for timing a duration. One of ordinary skill in the art would have been motivated to make such a combination in order to count towards a known time.

20. In re claim 14, Smith discloses a method for autonomous power sequencing in a computer system [fig.2; multiple processor combination 100] comprising:

- Receiving two or more server modules [server blades 102-1 and 102-n; fig.2].
- Providing a unique address for each server module based on the location of each server module on a midplane [0025-0026].
- Obtaining the unique address for each server module from the midplane [0029; determines unique address of each server module based on GA pins].

21. Smith did not disclose explicitly a power supply associated with the midplane and did not discuss the sequential supply of power to the server modules.

22. Bottom discloses a computer system [modular server system 100; fig.1] comprising:

- At least one power supply [130] associated with the midplane, the power supply operable to supply power to start up the server modules [server blades 110 require power in order to start up] [0021].

23. Bottom did not discuss the sequential supply of power to the server modules.

24. Tsurumi discloses a method comprising:

- At least one module [cross control circuit X] operable to calculate a start-up time [timer value] for each module [battery, power controller] based on the unique address for each module [col.38, l.56 – col.39, l.6].

Art Unit: 2116

- At least one module [cross control circuit X] operable to sequence power to the server modules based on the start-up times for each of the modules [col.38, l.56 – col.39, l.17; battery test expends power].

25. It would have been obvious to one of ordinary skill in the art, having the teachings of Smith, Bottom and Tsurumi before him at the time the invention was made, to modify the system taught by Smith to include the power supply taught by Bottom, in order to provide a more efficient use of the resources and minimize space [Bottom: 0021]. Furthermore, one of ordinary skill in the art would have modify the system taught by Smith and Bottom to include the sequential power supply teachings of Tsurumi, in order to obtain the claimed method. One of ordinary skill in the art would have been motivated to make such a combination as it provides a more efficient use of the resources and minimize space [Bottom: 0021] [Tsurumi: col.38, ll.4-15; col.39, ll.7-17].

26. As to claim 15, Smith, Bottom, and Tsurumi disclose each and every limitation of the claim as discussed above in reference to claim 2.

27. As to claim 16, Smith, Bottom, and Tsurumi disclose each and every limitation of the claim as discussed above in reference to claim 3.

28. As to claim 18, Smith, Bottom, and Tsurumi disclose each and every limitation of the claim as discuss in reference to claims 11 and 13.

29. As to claim 19, Smith, Bottom, and Tsurumi disclose each and every limitation of the claim as discussed above in reference to claim 4.

30. As to claim 20, Smith, Bottom, and Tsurumi disclose each and every limitation of the claim as discussed above in reference to claim 7.

Art Unit: 2116

31. As to claim 21, Smith, Bottom, and Tsurumi disclose each and every limitation of the claim as discussed above in reference to claim 9 [power supplied sequentially to conserve power].

32. As to claim 22, Smith, Bottom, and Tsurumi disclose each and every limitation of the claim as discussed above in reference to claim 9.

33. In re claim 23, Smith, Bottom, and Tsurumi disclose each and every limitation of the claim as discussed above in reference to claims 1 and 4-6. Furthermore, Bottom discloses the computer system comprising one of more chassis [mounting flanges 210] operable to house the server modules, the midplane, and the power supply [0022].

34. As to claim 25, the Examiner has taken Official Notice that it is well known in the art for one or more cabinets to house one or more of the chassis.

35. Claims 10, 17 and 24 are rejected under 35 U.S.C. 103(a) as being unpatentable over Smith, Bottom and Tsurumi as applied to claims 1 and 14 above, and further in view of Butka et al., U.S. Patent 6735704, hereinafter Butka.

36. In re claims 10 and 24, Smith, Bottom, and Tsurumi disclose each and every limitation of the claim as discussed above in reference to claims 1 and 14. Smith, Bottom, and Tsurumi did not discuss a management controller to provide redundant operation.

37. Butka discloses a system [10; fig.1] comprising a management controller [master controller 20] associated with the midplane [bus 22], the management controller operable to provide sequence redundancy by sequencing power to the server modules if the midplane experiences a failure [col.4, ll.8-51; col.5, l.34 – col.6, l.62].

Art Unit: 2116

38. It would have been obvious to one of ordinary skill in the art, having the teachings of Butka, Smith, Bottom, and Tsurumi before him at the time the invention was made, to modify the system taught by Smith, Bottom, and Tsurumi to include the redundancy teachings of Butka, in order to permit the system to continue normal operations in the event of a failure [Butka: col.1, ll.20-42]. One of ordinary skill in the art would have been motivated to make such a combination as it provides a way to permit the system to continue normal operations in the event of a failure.

39. In re claim 17, Smith, Bottom, and Tsurumi disclose each and every limitation of the claim as discussed above in reference to claim 14. Smith, Bottom, and Tsurumi did not disclose explicitly a multiplication factor.

40. Butka discloses a method wherein calculating the start-up time [delay seconds] comprises:

- Obtaining a multiplication factor [power subsystem number-1] for each server module [power nodes] and calculating the start-up time using the multiplication factor [col.4, ll.8-39].

41. It would have been obvious to one of ordinary skill in the art, having the teachings of Butka, Smith, Bottom, and Tsurumi before him at the time the invention was made, to modify the system taught by Smith, Bottom, and Tsurumi to include the multiplication factor teachings of Butka, in order to avoid simultaneous power supplies [Butka: col.1, ll.8-39]. One of ordinary skill in the art would have been motivated to make such a combination as it provides a way to avoid simultaneous power supplies.

Art Unit: 2116

42. Claims 12-13 are rejected under 35 U.S.C. 103(a) as being unpatentable over Smith, Bottom and Tsurumi as applied to claim 1 above, and further in view of Duley, U.S. Patent 6766222.

43. In re claim 12, Smith, Bottom, and Tsurumi disclose each and every limitation of the claim as discussed above in reference to claim 1. Smith, Bottom, and Tsurumi did not disclose explicitly switches associated with the server modules.

44. Duley discloses a system [power sequencing system] comprising a switch associated with each server modules [slave] and the at least one address module [master], the switch operable to accept a command from the address modules to switch between an on position and an off position [col.6, ll.18-39; col.6, l.50 – col.7, l.49].

45. It would have been obvious to one of ordinary skill in the art, having the teachings of Duley, Smith, Bottom, and Tsurumi before him at the time the invention was made, to modify the system taught by Smith, Bottom, and Tsurumi to include the switches of Duley, in order to provide cost savings in power supplies [Duley: col.7, l.50 – col.8, l.4]. One of ordinary skill in the art would have been motivated to make such a combination as it provides a way to provide cost savings in power supplies.

46. As to claim 13, Tsurumi discloses the system where at the expiration of the start-up time the address module [cross control circuit X] switches a selected switch to the on position allowing an associated server module [battery] to receive power from the power supply [col.38, l.56 – col.39, l.17; switch on for battery test which consumes power].

Response to Arguments

Art Unit: 2116

47. Applicant's arguments filed April 13, 2005 have been fully considered but they are not persuasive.

48. In re claims 1, 14, and 23, Applicant alleges that Tsurumi “fails to teach the sequential supply of power to start up a server module, but instead the portions of Tsurumi cited ... relate to battery tests performed on batteries”. Firstly, in response to applicant's arguments against the references individually, one cannot show nonobviousness by attacking references individually where the rejections are based on combinations of references. See *In re Keller*, 642 F.2d 413, 208 USPQ 871 (CCPA 1981); *In re Merck & Co.*, 800 F.2d 1091, 231 USPQ 375 (Fed. Cir. 1986). In the instant case, Examiner submits that the rejections were based on a combination of Smith, Bottom and Tsurumi to teach the sequential supply of power to start up a server module. In brief, Bottom taught the supplying of power to start up server modules [110; 0021]; and Tsurumi taught the sequential activation of [general] modules [power controller, battery; col.38, 1.56 – col.39, 1.17]. Secondly, it has been held that a prior art reference must either be in the field of applicant's endeavor or, if not, then be reasonably pertinent to the particular problem with which the applicant was concerned, in order to be relied upon as a basis for rejection of the claimed invention. See *In re Oetiker*, 977 F.2d 1443, 24 USPQ2d 1443 (Fed. Cir. 1992). In this case, Tsurumi clearly deals with the field of computer systems [col.1, ll.10-30] and is involved with sequential issues to avoid problems associated with simultaneous competition [col.38, ll.56-63]. Applicant alleges that Tsurumi “provides no disclosure, teaching or suggestion of calculating a start-up time for each server based on the unique address of each server module... instead, the timer of Tsurumi is set based upon the address of the associated power controller. Examiner submits that Tsurumi was cited to teach the *calculating of a start-up time for each*

Art Unit: 2116

[general] module based on a unique address, to be combined with the *server module* teachings of Smith and Bottom. Applicant alleges that Tsurumi “is clearly directed towards using batteries as a back-up...” Examiner submits that Bottom was cited to teach the power supply while Tsurumi was cited for other teachings related to the batteries as one of the *[general]* modules.

49. In re claim 17, Applicant alleges that Examiner’s rejection to claim 17 “fails to particularly address the use of a multiplication factor”. Examiner disagrees as the rejection does address the limitation of obtaining a multiplication factor [power subsystem number-1] for each server module [power nodes] and calculating the start-up time using the multiplication factor [Butka: col.4, ll.8-39].

50. All other claims were not argued separately.

Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Tse Chen whose telephone number is (571) 272-3672. The examiner can normally be reached on Monday - Friday 9AM - 5PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner’s supervisor, Lynne Browne can be reached on (571) 272-3670. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.


LYNNE H. BROWNE
SUPERVISORY PATENT EXAMINER
TECHNOLOGY CENTER 2100